

Solution

	org	0x00	
	goto	int	
int	bcf	STATUS, RP1	bank 1
	bsf	STATUS, RP0	
	movlw	0xff	TRISA = xxxx xxx1
	movwf	TRISA	
	movlw	0x00	TRISB = xxxx 0000
	movwf	TRISB	
	bcf	STATUS, RP0	bank 0
boucle	clrf	COMPT	0 → COMPT
b2	movf	COMPT, w	COMPT → W
	movwf	PORTB	W → PORTB
b1	btfsf	PORTA, 0	tester si RA0 =0
	goto	b1	
	incf	COMPT, f	incrémenter COMPT
	movf	COMPT, w	COMPT → W
	sublw	0x0A	10-W → W
	btfsf	STATUS, Z	si Z=1 signifiant si COMPT=10
	goto	b2	
	goto	boucle	
	END		